# Fayçal MOUNAIM, Ph.D.

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IEEE member

			Education			
	Electrical E	ngineering –	Microelectronics			
2007-2013 2003-2006	Ph.D M.Sc.A,	Ecole Polytechnique / Polystim Lab.		Montreal, Canada.		
2000	B.Eng,	INSA (Nation	al Institute of Applied Sc	iences)	Lyon, France.	
Experience						
2003-2013	M.Sc.A & Ph.D		Ecole Polytechnique / Polystim Lab. Montreal, Canada		Montreal, Canada.	
	Project:	Highly-integra micturition in I	ed neurostimulator and ne paraplegics	w stimulation	strategy to improve	
	<ul> <li>Defined new requirements with neurosurgeons and urologists.</li> <li>Proposed solutions to previous prototypes limitations.</li> <li>Implemented new functionalities such as telemetry and battery recharge.</li> <li>Improved encapsulation and packaging techniques.</li> <li>Designed several prototypes using discrete components.</li> <li>Validated prototypes with animal experiments.</li> <li>Patented a new stimulation strategy.</li> <li>Proposed a new inductive power recovery front-end for better power efficiency.</li> <li>Implemented this front-end in high-voltage CMOS 0.8um technology.</li> <li>Designed a miniature prototype based on custom HV ICs.</li> <li>Collaborated in the design of several ICs in CMOS 0.18um technology.</li> <li>Supervised about 30 interns, coached 10 M.Sc.A/1 Ph.D students.</li> <li>Taught "Electronics for communication" course.</li> <li>Supervised "Analog &amp; Mixed Microelectronics" course lab.</li> <li>Presented papers in several conferences.</li> </ul>					
2006 (3months) 0 0			<b>LTRIM Technologies in</b> g integrated circuits dedica ans.		Laval, Canada. nanagement.	
2004-2005 (12 months)	Research Asso Design Leade		Ecole Polytechnique / P Victhom Human Bionic	•	Montreal, Canada. Quebec, Canada.	
	Project:	Technology tra	nsfer of an implantable bl	adder neurosti	mulator prototype.	
	Managed and Reviewed and Defined new s Developed a n Selected appro Designed anal- <b>Tested, debug</b>	I planned a chal studied past sys specifications with ew architecture. opriate componen- og parts and sup gged and deliver	f 3 M.Sc.A student3/1 tecl lenging 6-month project. tem architectures. h client. hts based on specifications ervised digital and PCB de red functional prototypes erred technology knowled	and constrain esigns. 5 <b>on time.</b>	ts.	

2000-2003 (28 months)	Development of	r, <b>PHILIPS Semiconductors</b> oelectronics Circuits and Systems, RF integrated de circuits dedicated to TV tuners. Cable Fully Integrated Silicon Tuner 50-900MHz	Caen, France.			
	Position I	Product: TDA8270				
	• Project transfer f	rom Philips Natlab research center (6 months)	Eindhoven, Holland.			
	Designed and de	eveloped key analog building blocks.				
		Optimized architecture and RF/PLL interface. Transferred knowledge and completed product within Caen design team. Evaluated and tested the fabricated product (PCB, measurements and characterisation).				
		resolved challenging issues (simulations and theory	etical analysis).			
		roduct qualification plan.				
	• Conducted meeti	ings and presented achieved results to more than 50	) people.			
	Position II	Product: TDA8272				
		efinition of new specifications.				
		Reviewed and improved system design approaches. Documented design and technical know-how.				
		test, and PCB technicians, and supervised interns.				
	• Cooperated with	internal and external clients and collaborators.				
2000	Final year projec	t, INSA / PHILIPS Semiconductors	Lyon, France.			
(6 months)	Researched and s	studied new technology solutions for higher integra	tion of TV tuners.			
1999	Intern,	Ecole Polytechnique / Polystim Lab.	Montreal, Canada.			
(2 months)	Designed an inte	grated low-frequency oscillator for implantable dev	vices.			
1998	Intern,	ST-Microelectronics	Casablanca, Morocco.			
(1 month)	Programmed a m	icroprocessor-based control unit for production line	e monitoring.			
Professional training courses						

2010 (3 days)	Cadence - RF System-in-Package Methodology, CMC Microsystems.	
2002 (1 week)	Modern Analog Electronics: HF & IF Techniques, Philips CTT.	
	Architectures of receiver systems, System design aspects, Novel integrated architectures,	
	Basic properties of RF circuits, Noise and Non-linear distortion, Passive and RC Active filters	
	Fixed and Tuned filters, Oscillators, Mixers, Synthesizers.	
2002 (1 week)	Cadence - Spectre RF v4.4.3	
2001 (1 week)	Agilent - Technologies ADS v1.5	
	Advanced Design System Fundamentals & Communication Systems Design	
	Circuit simulation fundamentals, Circuit modeling, Parameter tuning and optimization.	
	S-Parameters, Harmonic Balance, Envelope Simulation, System design and co-simulation.	

## 2001 (1 day) **Philips ED&T - Adapt v3.0**,

Optimization to reach design specifications over process, temp. & power supply variations.

## Software tools

Cadence Environment and Tools (Spectre RF, Layout XL, DRC, LVS, SPW...), ADS, Matlab. MS-Office, MS-Visio, MS-Project.

## **Activities / Interests**

Amateur astronomy, electronics, photography, reading.

Biking, running, swimming, skating, soccer, most racquet games, and Kung-fu.

## **Publications**

#### Journal papers with reviewing committee:

- [1] F. Mounaim and M. Sawan, "Toward A Fully Integrated Neurostimulator With Inductive Power Recovery Front-End," Biomedical Circuits and Systems, IEEE Transactions on , vol.6, no.4, pp.309-318, Aug. 2012.
- [2] F. Mounaim and M. Sawan, "Integrated high-voltage inductive power and data-recovery front end dedicated to implantable devices," Biomedical Circuits and Systems, IEEE Transactions on, vol. 5, no. 3, pp. 283-291, June 2011.
- [3] F. Mounaim, E. Elzayat, M. Sawan, J. Corcos, and M.M. Elhilali, "New neurostimulation and blockade strategy to enhance bladder voiding in paraplegics," Contemporary Engineering Sciences, Hikari Ltd, vol. 3, no. 7, pp. 321–337, Sept. 2010.

## **Book chapter:**

[4] F. Mounaim and M. Sawan, "New neurostimulation strategy and corresponding implantable device to enhance bladder functions," in Biomedical Engineering, Trends in Electronics, Communications and Software, Anthony Laskovski, Ed.: InTech, 2010.

## Patent:

[5] F. Mounaim and M. Sawan, "Sacral neurostimulation to induce micturition in paraplegics," Patent application number US 12/886,552, Sep. 2010.

## Conference papers with reviewing committee:

- [6] F. Mounaim and M. Sawan, "High-voltage DC/DC converter for high-efficiency power recovery in implantable devices," in Microelectronics (ICM), 2009 International Conference on, Marrakech, 2009, pp. 22-25.
- [7] F. Mounaim, M. Sawan, and M. El-Gamal, "Integrated inductive power and data recovery front-end dedicated to implantable devices," in Biomedical Circuits and Systems Conference. BioCAS. IEEE, Beijing, 2009, pp. 229-232.
- [8] F. Mounaim, E. Elzayat, M. Sawan, J. Corcos, and M.M. Elhilali, "New sacral neurostimulation strategy to enhance micturition in paraplegics: Acute dog experiments," in Proc. 13th Ann. Int. Conf., Freiburg, 2008, pp. 22-24.
- [9] F. Mounaim, M. Sawan, and M. El-Gamal, "Fully-integrated inductive power recovery front-end dedicated to implantable devices," in Biomedical Circuits and Systems Conference. BioCAS. IEEE, Baltimore, 2008, pp. 105-108.
- [10] F. Mounaim, M. Sawan, and S. Bedard, "Implantable neuro-monito-stimulation system dedicated to enhance the bladder functions," in Biomedical Circuits and Systems Conference. BioCAS. IEEE, London, 2006, pp. 198-201.
- [11] F. Mounaim and M. Sawan, "Implantable Electronic Device Dedicated to Neural Stimulation and Sensing," Computer Architecture for Machine Perception and Sensing. CAMP. International Workshop on, Montreal, 2006, pp.196-197.
- [12] F. Mounaim, Y. Laaziri, G. Lesbros, P. Nadeau, É. Bharucha, M. Sawan, and S. Bédard, "Implantable neurostimulator for bladder rehabilitation in paraplegics," in 10th Annual Conference of the International Functional Electrical Stimulation Society (IFESS), Montreal, 2005.
- [13] F. Mounaim, Y. Laaziri, D.P.-Ferron, and M. Sawan, "Continuous and selective neurostimulation techniques to recuperate the bladder functions," National Spinal Cord Rehab Conference, Toronto, 2004.

Honors / Awards

Honors / Awarus					
2010 2007 - 2010 2006 & 2010 2001 1997 - 2000	Grand Prize for Innovation and Vulgarization contest at ACFAS congress. NSERC (Canada) 3 years Ph.D scholarship and several ReSMiQ/Polytechnique awards. Best graduate presentation at IEEE-Poly conference. Royal Philips Electronics Award: SILVER Design of the year. National (Morocco) merit scholarship				